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Sheet

1

of

2

Application Number

10/587.414
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Filing Date
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July 27, 2006
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First Named Inventor

Vincent W. Leung

Art Unit

Examiner Name

Attorney Docket Number

0321.72250
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**Examiner  
Signature**

/Khanh Nguyen/ (02/04/2008)

Date  
Cons

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02/04/2008

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>		<b>Complete if Known</b>			
		Application Number	10/587,414		
		Filing Date	July 27, 2006		
		First Named Inventor	Vincent W. Leung		
		Art Unit			
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	0321.72250

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/K.V.N./		E.Taniguchi, T. Ikushima, K. Itoh, N. Suematsu, "A Dual Bias-Feed Circuit Design for SiGe HTB Low-Noise Linear Amplifier," IEEE Trans. Microwave Theory Tech., Vol. 51, No. 2, Feb. 2003, pp. 414-421.	

Examiner Signature	/Khanh Nguyen/ (02/04/2008)	Date Considered	02/04/2008
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